




JSS Mahavidyapeetha
JSS Academy of Technical Education, Bengaluru
Department of Electronics & Communication Engineering



FACULTY PROFILE

1. Personal Details

NAME	Dr Kavitha M	
DEPARTMENT	Electronics & Communication Engineering	
DESIGNATION	Assistant Professor	
PHONE	9449238353	
EMAIL ID	mkavitha@jssateb.ac.in	
TEACHING EXPERIENCE	23 Years	
INDUSTRY EXPERIENCE	Nil	
RESEARCH EXPERIENCE	8 Years	

2. Qualification

COURSES	SPECIALIZATION	INSTITUTION	UNIVERSITY
Ph D	VLSI Design	BMSCE, Bangalore	VTU
M.Tech	VLSI Design & Embedded Systems	BMSCE, Bangalore	VTU
BE / Diploma / ITI	Electronics & Communication Engineering	MVIT, Bangalore	Bangalore
PUC	PCMB	JNV, Mandya	CBSE
SSLC	X Standard	JNV, Mandya	CBSE

3. Membership of Professional Bodies:

1. Life Membership in Indian Society for Technical Education (MISTE)
2. IEEE member

4. Awards

Award Title	Date of receiving the award	Award issuing authority/ Body / Organization

5. Publications (Journals Conferences)

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	Design and Implementation of a Second Order Continuous-Time $\Sigma\Delta$ Modulator for	M Kavitha, S Akhila, A Kannan	Engineering, Technology & Applied Science Research	Vol. 13 (1), 10128-10133	WoS & Scopus	1.5	Dionisios Pylarinos

	ECG Signal Acquisition						
2	A comprehensive review on Machine Learning algorithms for prediction and analysis of Chronic Kidney Disease	Kavitha M , Saroja S Bhusare ,Ananya R , Jayashree P , L Ashwini , Niharika K	High Technology Letters	ISSN NO : 1006-6748			
3	Analysis and Design of different OTA topologies for Bio-potential signals	Kavitha M, Dr S Akhila	International Journal of future generation and communication Networking	Vol.14,No.1,2021	WoS	0.41	SERSC
4	Design and Performance analysis of a Low Power First order sigma Delta modulator	Kavitha M, Dr. H B Bhuvaneshwari	International Journal of Applied Engineering Research	Online ISSN : 0973-4562 Volume No. : 10 Issue No. : 92 Month & Year : 2015 Page Nos. : 284-290	Scopus	0.14	
5	An 8-bit area optimized low power continuous time band pass sigma-delta ADC in 45nm CMOS	Kavitha M,Rahul	International Journal of Information Technology and Computer Engineering	Online ISSN : 2319-6890 Volume No. : 4 Issue No. : 3 Month & Year : May 2015 Page Nos. : 155-159			
6	FPGA Implementation of Four-Channel Bit Error Rate Tester for Spacecraft Data Acquisition System	Kavitha M , Sinchana	International Journal of Information Technology and Computer Engineering	e- ISSN : 2455-529 Month & Year : 2016 Page Nos. : 109-113			

6. Grants/Funding Received

Grant Amount	Project Name	Date of receiving the Grant	Grant issuing authority/ Body / Organization

6.Others :Workshops / Conference (Orgained/Attended)

6.a. Workshops / Conference attended

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	A machine learning based power load	S Jyothi, S Mallikarjunaswamy, M	2023 International Conference on Recent		Scopus		

	prediction system for smart grid energy management	Kavitha, N Kumar, N Sharmila, ...	Advances in Science and Engineering ...				
2	Restoration of Images Corrupted by Multipath Fading Channel with Weighted Encoding	V Yatnalli, SS Bhusare, M Kavitha, A Naik, T Ashwini, D Chandana	2022 Fourth International Conference on Emerging Research in Electronics		WOS & Scopus		
3	Flexible Analog Front End Architecture for Biomedical Applications	M Kavitha, S Akhila, Anand Kannan	2021 IEEE 3rd PhD Colloquium on Ethically Driven Innovation and Technology		WOS & Scopus		
4	Switched Capacitor Implementation of First Order Sigma-Delta Modulator	Kavitha M, Dr S Akhila	2018Third International Conference on Electrical, Electronics, Communication, Computer Technologiesand Optimization Techniques (ICECCOT)14-15, December 2018				
5	FPGA Implementation of Four-Channel Bit Error Rate Tester for Spacecraft Data Acquisition System	Kavitha M , Sinchana	4 th National Conference on "Recent Advances in Science, Engineering and Technology (NCRASET-16)" organized by Sri Venkateshwara College of Engineering, Bangalore.				
6	An 8-bit area optimized low power continuous time band pass sigma-delta ADC in 45nm CMOS	Kavitha M,Rahul	2 nd International Conference on Convergent innovative Technologies ICCIT-2015 held at Cambridge Institute of Technology, Bangalore				
7	FPGA implementation 4-channel bit error rate tester for space trak data aquasition system	Kavitha M, Madhusudhan	NCRASET-16				

6.b. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or anyother sponsoring body)

6.c. workshop/ Seminar /Conference Attended (those NOT sponsored by AICTE / ISTE/IETE/TEQIP or anyother sponsoring body)

Sl. No.	Name of the workshop / Conference	Organiser	Date
1	Research trends and applications of Nanoscale devices and circuits	Department of Electronics and Communication Engineering, NIT,Warangal	20 th to 31 th July 2024
2	Inculcating Universal Human Values in Technical Education	All India Council for Technical Education (AICTE)	12th June to 16th June 2023

3	Analog VLSI Design using EDA Tools	Visvesvaraya Technological University, Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur (Dist.)	20th to 25th March 2023
4	IMPORTANCE OF DATA SCIENCE IN MACHINE LEARNING AND ITS APPLICATIONS	RV Institute of Technology and Management, Bengaluru	6th - 10th Feb 2023
5	Design of low power analog integrated circuits	Gauhati University Institute of Science and Technology	29/11/2021 to 03/12/2021 , 5 days
6	The Science of Well-Being	Coursera ,Yale University	18/10/2021 to 29/12/2021 , 6 weeks
7	Machine Learning for all	Coursera,University of London	06-08-2020 (4 weeks)
8	Six days short term programming programme on Industry 4.0 sponsored by AICTE	Department of Electronics and Communication Engineering, ACSCE, Bangalore	9 th to 14 th November 2020
9	AI and ML applications in Image processing using modern tools	M S Ramaiah Institute of technology, Bangalore	13 th to 18 th July 2020
10	Three Day Faculty Development Program on "Analog and Digital System Design using Cadence Tool"	Department of Electronics and Communication Engineering, GSSSIETW, Mysore	23 rd to 25 th January 2019
11	5 Days Faculty Development Program on "GNU Radio and Software Defined Radio"	Department of Electronics & Communication Engineering at JSSATE, Bangalore.	27 th June 2016 to 1 st July 2016
12	one week workshop on "Electronics System Design, Manufacturing & Testing(ESDMT)"	Department of Electronics& Instrumentation Engineering at BMSCE, Bangalore in association with Entuple Technologies.	27 th to 31 st July 2015
13	Didactic Seminar on "Cadence OrCAD PSPICE and ORCAD PCB".	Department of Electronics& Instrumentation Engineering at BMSCE, Bangalore	31 st July 2015
14	"Signal, Image Processing and SDR using LabView".	Department of Electronics & Communication Engineering at JSSATE, Bangalore.	22 nd to 24 th June 2015
15	3 day Faculty Development Program on "Advanced VLSI Design using Cadence Tool Suite"	Department of Electronics & Communication Engineering at JSSATE, Bangalore.	16 th , 17 th & 18 th July 2014
16	5 day Hands-on workshop on "Analog & Digital Chip Design Using Cadence".	Department of Electronics& Communication Engineering at Nitte Meenakshi Institute of Technology, Bangalore	22 nd to 26 th July 2014
17	Participated as a Delegate in the 26 th Indian Engineering Congress.	Organized by Indian Engineering Congress, Bangalore.	15 th to 18 th December 2011

6.d. PROJECT / Innovations/Patents

1. Design Of High Resolution Sigma-Delta Modulator For ECG Recordings
2. Machine Learning Based Audio Device for Teaching Instruction Messages

6.e. Any other information you will like to share about your professional experience